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### CMOS analog circuit design | Allen, Phillip E.; Holberg ...

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**Razavi-3930640 raz24936`FM`00i-xviii December 18, 201510:37 i**

CMOS Analog Circuit Design © P.E. Allen - 2016 DESIGN EXAMPLE OF A TWO-STAGE OP AMP Example 23-1 - Design of a Two-Stage Op Amp If  $K_N = 120\mu A/V^2$ ,  $K_P = 25\mu A/V^2$ ,  $V_{TN} = |V_{TP}| = 0.5 \pm 0.15V$ ,  $N = 0.06V^{-1}$ , and  $P = 0.08V^{-1}$ , design a two-stage, CMOS op amp that meets the following specifications.

**LECTURE 23 DESIGN OF TWO-STAGE OP AMPS**

Design of Analog CMOS Integrated Circuits by Behzad Razavi, deals with the analysis and design of analog CMOS integrated circuits, emphasizing fundamentals, as well as new paradigms that students and practicing engineers need to master in today's industry.

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"Analog circuit design is like chess - just because you know how the pieces move doesn't mean you know how to play the game". - Patrick Lahey. This course is a continuation of another Analog IC Design "CMOS Analog IC Design - MOSFET, DC BIAS and Transconductance".

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**Practical Circuit Examples I (Chapter 5) - Systematic ...**

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